

Matrox Mura IPX 4K IP Decode and Display Cards



4K IP Decode and Display Allows Consolidation of Streams into High-Density Multiviewers & Personal Video Walls

Matrox® Mura IPX 4K IP decode and display cards empower integrators to create high-density, IP-based multiviewers and personal video walls. These cards are designed for use in control rooms, process monitoring, and security applications that require high-density decode and precise control.

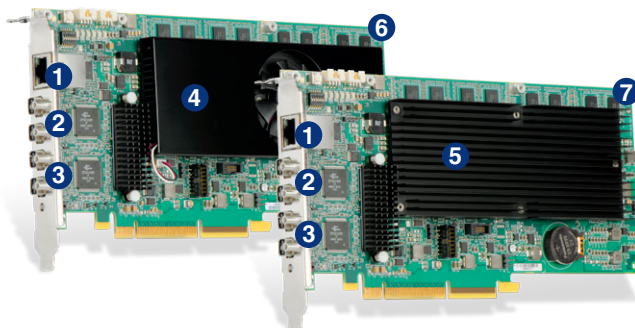
Benefit from IP decoding plus four DisplayPort™ outputs on a single-slot PCI Express® card for simplified integration and cost savings. Allowing for 4K video output in 32-bit color on up to four 4K monitors means the Mura IPX decode and display card can create stunning video walls running at an effective 8K resolution. Moreover, Mura IPX works with the resolutions you require, decoding up to two 4K60, four 4K30, eight 1080p60, sixteen 1080p30, or a multitude of SD channels.



Intuitive Matrox MuraControl™ software allows the management of Mura IPX decode and display powered multiviewers from a console system or remotely using the MuraControl for Windows® software or MuraControl for iPad® app. Matrox also offers a variety of APIs that allow OEMs and developers to deploy intuitive, ready-to-use software or build custom interfaces and applications.

Added functionality specifically for multiviewer applications has been integrated into the cards, with text overlay to indicate window titles, stream sources, locations, or zones, and onscreen clock providing a temporal reference for accurate stream monitoring.

Available in passive and active cooling options, the low-footprint Mura IPX decode and display cards provide unprecedented system design flexibility to deliver high-performance multiviewers, video wall controllers, and personal video walls that can be tailored to meet specific project requirements.



- 1) Work off a dedicated AV network, relieving demand on host systems
- 2) 4x Mini DisplayPort for up to 4x 4K30 displays
- 3) Prevent loose cabling with secure Mini DisplayPort connectors
- 4) Powered-fan option allows active cooling in high demand environments
- 5) Fanless, passively-cooled option with no moving parts for even greater reliability
- 6) MURAIPXO-D4LF
- 7) MURAIPXO-D4LHF (Fanless)

Matrox Mura IPX 4K IP Decode & Display Cards

	4K IP Decode & Display Card (part number: MURAIPXO-D4LF)	4K IP Decode & Display Card (Fanless) (part number: MURAIPXO-D4LHF)
Product		
Board Type	Four Outputs and IP Decode	
Connectors	4 x Mini DisplayPort, 1 x 100/1000 Base-T RJ45 Ethernet Port	
DisplayPort¹ Output Resolutions	4096x2160 @30Hz, 3840x2160 @30Hz, 2560x1600 @60Hz	
Bus Interface	PCIe x16 (Gen2) - Power only	
Memory	8 GB (34 GB/sec)	
Network Interface		
Standard	Ethernet 10/100/1000 Base-T, Auto-Detect, Half/Full-Duplex	
Connector	RJ45	
IP Version	IPv4/IPv6	
Distribution Method	Unicast, Multicast and Multiple Unicast	
IP Addressing	DHCP (Default) and Static IP	
Streaming & Control Protocols		
Streaming Protocols	SRT, RTP, RTSP, MPEG2-TS, RTMP¹	
Command & Control Protocols	Telnet and HTTP/HTTPS	
Color Space		
Pixel Transfer Formats	RGB: 8:8:8, 10:10:10 (24/32 bits per pixel), YUV: 4:4:4, 4:2:2, 4:2:0 (8/10 bits per component), MONO: (8/10 bits per pixel), Color Space Conversion Support	
Video & Audio Processing		
Video Scaling	Matrox Advanced MultiTap Video Scaling Engine for 4K to SD multi-channel downscaling and SD to 4K multi-channel upscaling	
Video Deinterlacing	Adaptive Deinterlacer and Antialiasing Technology	
Video Compositing	Multi-Channel Video Composite/Key/Blend/Crop/Mirror/Flip	
Audio Format	AAC, PCM, Stereo and Mono	
Audio Sampling Rate	Between 32 KHz and 96 KHz	
Video Decoding		
Codec Engine	H.264/MPEG-4 Part 10 (AVC), Up to Level 5.2	
H.264 Profiles	Baseline profile (BP), Main Profile (MP), High Profile (HiP), High 10 Profile (Hi10P), High 4:2:2 Profile (Hi422P), High 4:4:4 Predictive Profile Separate Plane (Hi444PP)	
H.264 Decode¹	Two 3840x2160 @60Hz, four 3840x2160 @30Hz, eight 1920x1080 @60Hz, sixteen 1920x1080 @30Hz, numerous SD IP channels	
Decoder Bitrates	100 Kbps to 500 Mbps CABAC, 100 Kbps to 800 Mbps CAVLC	
Rate Control	Constant Bitrate (CBR), Variable Bitrate (VBR), VBR with Constraints, Configurable GOP (Group Of Pictures) Structure	
Environmental & Power Conditions		
Operating Conditions	Temperature: 0 to 45 degrees Celsius, Humidity: 20% to 80% non-condensing, Altitude: from 650 hPa (3580m) to 1013 hPa (0m)	
Non-operating/Storage Conditions	Temperature: -40 to 70 degrees Celsius, Humidity: 10% to 90% non-condensing, Altitude: from 192 hPa (12000m) to 1020 hPa (-50m)	
Typical Power Consumption	24.6W (12V), 6.105W (3.3V) [Total: 30.705W]	
General Specifications		
Dimensions² (L x H) / Weight	9.02 x 4.38 inches / 314g	9.02 x 4.38 inches / 278g
Regulatory / Environmental Certifications	FCC Class B, CE Class B, ACMA Class B, VCCI Class B, MSIP, ICES - 003 Class B, CSA / EU RoHS, China RoHS, REACH	
Warranty	2 Years	3 Years

1. Standard resolutions listed above. Support for custom resolutions available. For specific requirements, contact Matrox.

2. Including gold-fingers; not including bracket and connectors.

3. Feature not yet supported. Please view Release Notes for latest supported protocols.

Contact Matrox

graphics@matrox.com | North America Corporate Headquarters: 1 800-361-1408 or 514-822-6000

London Office: +44 (1895) 827300 or +44 (0) 1895 827260

Serving: United Kingdom, Ireland, Benelux, France, Spain, Portugal, Middle East, Africa

Munich Office: +49 89 62170-444

Serving: Germany, Austria, Switzerland, Denmark, Finland, Norway, Sweden, Central and Eastern Europe, the Baltic States, Greece, Turkey, Italy

© 2019 Matrox Graphics, Inc. All rights reserved. Matrox reserves the right to change specifications without notice. Matrox and Matrox product names are registered trademarks in Canada or other countries and/or trademarks of Matrox Electronic Systems, Ltd and/or Matrox Graphics Inc. All other company and product names are registered trademarks and/or trademarks of their respective owners. 10/2019

matrox[®]
Graphics for Professionals